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Since the early days of logic programming, researchers in the field realized the potential for exploitation of paral
transparency, among other characteristics, make logic programs interesting candidates for obtaining speedups tl
irregular computatio ...

Keywords: Automatic parallelization, constraint programming, logic programming, parallelism, prolog

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Daniel C. Swinehart, Polle T. Zellweger, Richard J. Beach, Robert B. Hagmann
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This paper presents an overview of the Cedar programming environment, focusing on its overall structure—that
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5

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Data sets in large applications are often too massive to fit completely inside the computers internal memory. The be a major performance bottleneck. In this article we survey the state of the art in the design and analysis of ex consider a varie ...

Keywords: B-tree, I/O, batched, block, disk, dynamic, extendible hashing, external memory, hierarchical memo

6 Draft Proposed: American National Standard—Graphical Kernel System
Technical Committee X3H3 - Computer Graphics
February 1984   **ACM SIGGRAPH Computer Graphics**, Volume 18 Issue SI

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Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagra event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do n non-trivial commun ...

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This paper discusses the design considerations for the architectural extensions that distinguish System/370 from them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, progr input/output operations. ...

Keywords: architecture, computer systems, error handling, instruction sets, virtual storage

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10 The structure of Cedar
Daniel C. Swinehart, Polle T. Zellweger, Robert B. Hagmann
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This paper presents an overview of the Cedar programming environment, focusing primarily on its overall structu

single programming language, also called Cedar. We will emphasize the extent to which the Cedar language, wit Laboratory (CS ...

11 A dynamic view-oriented group communication service
Roberto De Prisco, Alan Fekete, Nancy Lynch, Alex Shvartsman
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Luiz André Barroso, Kourosh Gharachorloo, Robert McNamara, Andreas Nowatzyk, Shaz Qadeer, Barton Sano, Scotl
May 2000   **ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual int**

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The microprocessor industry is currently struggling with higher development costs and longer design times that i are especially ill suited for important commercial applications, such as on-line transaction processing (OLTP), wh constitute by fa ...

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E. N. Houstis, J. R. Rice, S. Weerawarana, A. C. Catlin, P. Papachiou, K.-Y. Wang, M. Gaitatzes
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The article presents the software architecture and implementation of the problem-solving environment (PSE) PEl PELLPACK incorporates many PDE solving systems, and some of these, in turn, include several specific PDE solvi problems, Since a PSE should p ...

Keywords: PDE language, execution models, knowledge bases, libraries, parallel reuse methodologies, problem

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M. H. Eich, David L. Wells
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A specialized data flow graph, Database Flow Graph (DBFG) is introduced. DBFGs may be used for scheduling da intratransaction dependencies, and is constructed from the Transaction Flow Graphs (TFG) of active transactions

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Norman Meyrowitz, Andries van Dam
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16 "Topologies"—distributed objects on multicomputers
Karsten Schwan, Win Bo
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Application programs written for large-scale multicomputers with interconnection structures known to the progra structures implement a wide variety of functions, including the exchange of data or control information relevant program control, and so o ...

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Lingkun Chu, Hong Tang, Tao Yang, Kai Shen
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Large-scale cluster-based Internet services often host partitioned datasets to provide incremental scalability. The
paper presents the design and implementation of a programming primitive -- Data Aggregation Call (DAC) -- to
reduction ope ...

Keywords: cluster-based network services, fault tolerance, load-adaptive tree formation, response time, scalab

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Haitham Akkary, Michael A. Driscoll
November 1998 **Proceedings of the 31st annual ACM/IEEE international symposium on Microarchitecture**

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19 Compiling nested data-parallel programs for shared-memory multiprocessors
Siddhartha Chatterjee
July 1993   **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 15 Issue 3

Full text available: pdf(4.17 MB)   Additional Information: full citation, references, citings, index terms, review

Keywords: compilers, data parallelism, shared-memory multiprocessors

20 The family of concurrent logic programming languages
Ehud Shapiro
September 1989   **ACM Computing Surveys (CSUR)**, Volume 21 Issue 3

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